

**INFORMATION DISCLOSURE
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Applicant(s): Hall et al.

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U.S. PATENT DOCUMENTS

Examiner Initials	Document Number (Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant	Class	Sub-class
DV	US-5,524,286	06-04-1996	Chiesa et al.	455	126
DV	US-6,266,320 B1	07-24-2001	Hedberg et al.	370	206
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FOREIGN PATENT DOCUMENTS

Examiner Initials	Document Number (Country Code-Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name Of Patentee of Applicant	Translation? Yes/No/n/a
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OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)

DV	Andraka, Ray, "A survey of CORDIC algorithms for FPGA based computers",
	FPGA1998, Monterey, CA, 10 pages

Examiner's Signature: /Don Vo/

Date Considered: 08/23/2006

Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered.
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